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June 20, 2003	Luci M. Arevalo	Muil Our	June 20, 2003	
Date Mailed	Name	Signature	Date	

Attorney's Docket No: 42.P15050

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:)		
Hon-Mo Raymond Law et al.)	Examiner:	Not Yet Assigned
Serial No: Not Yet Assigned)	Art Unit:	Not Yet Assigned
Filing Date: Concurrently herewith)		
For: METHOD AND APPARATUS TO CONSTRUCT A FIFTY PERCENT (50%) DUTY CYCLE CLOCK SIGNAL ACROSS POWER DOMAINS)))		

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir/Madam:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the documents listed on thereon. It is respectfully requested that the listed documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

<u>X</u>	37 C.F.R. §1.97(b).
	37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is <u>one</u> of the following:
	A certification pursuant to 37 C.F.R. §1.97(e) or
	A check for \$ for the fee under 37 C.F.R. § 1.17(p).
	37 C.F.R. §1.97(d). If so, then enclosed with this Information Disclosure Statement are the following:
	(1) A certification pursuant to 37 C.F.R. §1.97(e);
	(2) A petition requesting consideration of the Information Disclosure Statement; and
	(3) A check for \$ for the fee under 37 C.F.R. §1.17(i) for submission of the Information Disclosure Statement.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 6/20/03

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Substitute for Form 1449A/PTO (Modified)		Complete if Known				
INFORMATION DISCLOSURE		Application Number	New Patent Application			
		Filing Date	Concurrently herewith			
SIAI		NT BY APF		First Named Inventor:	Raymond R. Law et al.	
(use as many sheets as necessary)		Group Art Unit	Not Yet Assigned			
				Examiner Name	Not Yet Assigned	
Sheet	1	of	1	Attorney Docket Number	42P15050	
		OTHER	R ART - NO PATE	NT LITERATURE DOCUMEN	TS	
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²	
	1.	YOUNG, I. A., "A PLL Clock Generator with 5 to 110 MHz of Lock Range for Microprocessors," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 27, No. 11, (November 1992), pp. 1599-1606				
	2.	WONG, K. L. et al., "Cascaded PLL Design for a 90nm High Performance Microprocessor," Intel Corporation, Hillsboro, Oregon				
			-			
						

Examiner	Date
Signature	Considered

^{*}Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

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